

PATENT ABSTRACTS OF JAPAN

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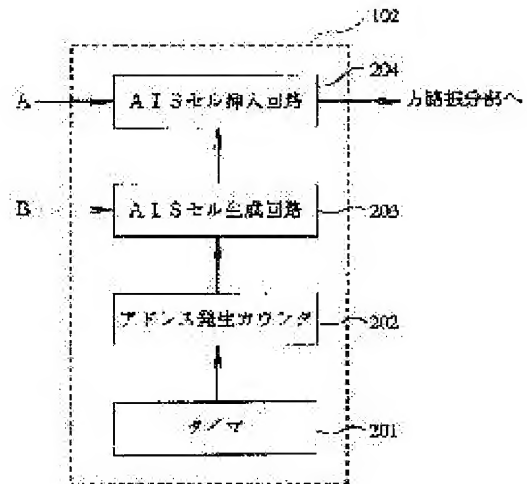
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(54) ATM SYSTEM

(57)Abstract:

PROBLEM TO BE SOLVED: To reduce the scale of hardware by eliminating the need for a connection setting table for an alarm indication signal.

SOLUTION: An address generating counter 202 conducts countup or countdown based on a period given from a timer 201 to generate sequentially virtual paths. An alarm indication signal AIS cell generating circuit 203 uses the virtual paths received from the address generating counter 202 to sequentially generate AIS cells. An AIS cell insertion circuit 204 transmits sequentially the AIS cells to a path distribution section for a user cell. An alarm generating section 102 generates alarm indication cells for all addresses and transmits them and invalid cells are discarded to a path distribution section for the user cell. Thus it is not required to provide the connection setting table to the alarm generating section 102.



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